

# Schedule

Date	DURATION	Topic	Speakers
DAY 1 22.08.2022 Monday	1.5 Hrs 10.00 AM – 11.30AM (IST)	Lightning Talk on Power ISA and curriculum	Dr. Peter Hofstee
DAY 1 22.08.2022 Monday	1 Hrs 11.30 AM – 12:30 PM (IST)	OpenPOWER based Computer Architecture Curriculum	VT
DAY 1 22.08.2022 Monday	30 minutes 12:30 PM – 1.00 PM (IST)	OpenPOWER	James Kulina
DAY 1 22.08.2022 Monday	1.00 Hr 1.00 PM to 2.00 PM (IST)	POWER ISA vs RISC V Comparison	Vinod
DAY 2 23.08.2022 Tuesday	1.00 Hr 10.00 AM – 11.00AM (IST)	IC design workloads running on Clouds	Dr Gi-Joon Nam
DAY 2 23.08.2022 Tuesday	30 Minutes 11.00 AM – 11.30 AM (IST)	LibreBMC	Todd
DAY 2 23.08.2022 Tuesday	1.5 Hrs 11.30 AM– 1.00PM (IST)	Microwatt Introduction	Prof. Satyadhyan
DAY 2 23.08.2022 Tuesday	1 Hr 1.00 PM – 2.00 PM (IST)	FPGA Implementation of Microwatt system demo	Prof. Satyadhyan
DAY 3 24.08.2022 Wednesday	2 Hrs 10.00 AM – 12 PM (IST)	GEM5 for processor evaluation	Ms.Kajal, IBM Systems
DAY 3 24.08.2022 Wednesday	2 Hrs 12.00 PM – 2.00PM (IST)	FPGA Implementation of Microwatt system Hands on (Lab Session-1)	Prof. Satyadhyan

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<b>Date</b>	<b>DURATION</b>	<b>Topic</b>	<b>Speakers</b>
DAY 4 25.08.2022 Thursday	2 Hrs 10.00 AM – 12 PM (IST)	Verification concepts - Introduction	Prof. Shashidhara
DAY 4 25.08.2022 Thursday	2 Hrs 12.00 PM – 2.00PM (IST)	System Verilog Essentials	Prof. Shashidhara
DAY 5 26.08.2022 Friday	1 Hr 10.00AM-11.00AM (IST)	Components of IP Core verification	Prof. Shashidhara
DAY 5 26.08.2022 Friday	1 Hr 11.00AM-12.00PM (IST)	Flow of the stimulus in IP verification environment	Prof. Shashidhara
DAY 5 26.08.2022 Friday	2 Hrs 12.00 PM – 2.00PM (IST)	Different testbench automation techniques in IP verification	Prof. Shashidhara
DAY 6 27.08.2022 Saturday	2 Hrs 10.00 AM – 12 PM (IST)	System on Chip (SoC) and its Components	Dr. Manikandan.N
DAY 6 27.08.2022 Saturday	2 Hrs 12.00 PM – 2.00PM (IST)	SOC tool chain environment and Microwatt SOC demo	Varun

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Date	DURATION	Topic	Speakers
DAY 7 29.08.2022 Monday	2 Hrs 10.00 AM – 12 PM (IST)	Coriolis 2 EDA tools	Jean Paul Chaput
DAY 7 29.08.2022 Monday	2 Hrs 12.00 PM – 2.00PM (IST)	Fabless SoC Ananth and SoC verification methodologies	Arjun
DAY 8 30.08.2022 Tuesday	2 Hr 10.00AM-12.00PM (IST)	Litex Flow	Pradyumna K OA Systems
DAY 8 30.08.2022 Tuesday	2 Hr 12.00PM-2.00PM (IST)	Libre-SOC FPGA to ASIC	Luke Leighton
DAY 9 01.09.2022 Thursday	1 Hr 10.00AM-11.00AM (IST)	Chip Alliance	Rob mains
DAY 9 01.09.2022 Thursday	1 Hr 11.00AM-12.00PM (IST)	3D ICs and Chiplets	Farhang
DAY 9 01.09.2022 Thursday	2 Hrs 12.00 PM – 2.00PM (IST)	OpenLANE Flow for GDS II Generation With Google Cloud	Johan, Google JP
DAY 10 02.09.2022 Friday	1 Hrs 10.00 AM – 11.00 AM (IST)	Automated Analog design flow	Dr. Mehdi
Day 10 02.09.2002 Friday	1 Hrs 11.00 AM –12.00 PM (IST)	Efabless Cloud	Dr Mohamed Kassem
DAY 10 02.09.2022 Friday	2 Hrs 12.00 PM – 2.00PM (IST)	Automated RTL to GDS II Flow. – (Lab Session-2)	Dr Satyadhyan