

About Academy

Electronics and ICT Academy (E&ICT) at IIT Roorkee (funded by Ministry of Electronics and Information Technology) aims to enrich and upgrade teaching and research competences of engineering faculties of institutes/colleges by conducting courses and workshops in fundamentals as well as emerging areas of E&ICT and enabled areas. The programs are conducted by well-known industry partners, resource persons from leading academia and experts from renowned R&D organizations.



Activities of the Academy

- Specialized training on basic and advanced level topics with hands-on experience in the emerging areas of Electronics & ICT.
- Setup the activity centers to conduct FDPs locally at institutes/colleges.
- Curriculum development for the industry.
- Continuing Education Programme for students/working professionals.
- Design, develop and delivery of specialized modules for specific research areas in industry.

Who Can Attend ?

Program is open to faculty members/research scholars/PG students from colleges/universities, and industry personnel working in the concerned/allied discipline.

Registration Fee

Faculty members: ₹ 1000/-
Research scholars: ₹ 500/-

Payment Details

DD in favor of "Dean SRIC IIT Roorkee" payable at Roorkee
"OR"

Make Online Payment by NEFT/RTGS on given detail

Account Name: Research Project, IIT Roorkee
Account Number: 33012172097
IFSC code: SBIN0001069

How to Apply

Step 1: Make Payment

Step 2: Participants may fill registration form through Academy website (<http://eict.iitr.ac.in>).
OR

Step 2: Send a duly filled-in registration form along with Demand Draft to Academy address.

Mr. Prateek Sharma, EICT Academy, ECE Department, IIT Roorkee-247667



Important Dates



**Last Date For Online
Registration :
15th Sep 2017**

**Last Date For Receiving
Application : 16th Sep 2017**



FACULTY DEVELOPMENT PROGRAM ON

"FPGA based SoC Design"

Under the aegis of

**Electronics & ICT Academy
IIT Roorkee**



In Association with

**Tula's Institute, The Engineering &
Management College Dhaulkot,
Chakrata Road, Dehradun
(Uttarakhand)**



Sep 20, 2017 - Sep 21, 2017

Experts from Academia/Industry

Dr. B.P. Das
ECE Dept.
IIT Roorkee

Ankur Sangal
CoreEL Technologies,
New Delhi

Dr. Sanjeev Manhas
ECE Dept.
IIT Roorkee

Supported by

Ministry of Electronics & Information Technology
Government of India



Venue

Tula's Institute, Dehradun

Objective of the Course

- Create and understand HDL Design using FPGA.
- Implement sequential and combinational design using Xilinx Vivado Tool.
- Xilinx Design Constraints.
- Debugging using Vivado Logic Analyzer cores.
- Simple Hardware Design.
- Adding Peripherals in Programmable Logic.
- Project implementation like UART & Traffic Light Controller.

Benefits and Outcomes of the Course

- Create and debug HDL designs.
- Configure FPGA architecture features, such as Clock Manager, using the Architecture Wizard.
- Pinpoint design bottlenecks using the reports.
- Utilize synthesis options to improve performance.

At the end of the program, participants will be able to:

- Upgrade the lab facilities in the parent institutes in areas of FPGA based design, image processing, wireless communications etc.
- Guide/Mentor the students to seek opportunities in application specific integrated circuits (ASIC), Electronics design automation(EDA), speech recognition, test & measurements etc.

Lab Platform/Boards to Used

- Windows
- XILINX VIVADO system edition
- ZedBoard Zynq™-7000 Development Board

Focus Areas

- Vivado Design Flow
- Xilinx Vivado Tool Flow with FPGA based coding technique.
- Xilinx Design Constraints.
- Timing constraints and perform the timing analysis.
- Vivado Logic Analyzer cores to debug/analyze system behavior.
- IP Integrator to develop a basic embedded system for a target board.
- Extend the hardware system by adding AXI peripherals from the IP catalog.
- IP Integrator and Embedded system design flow.

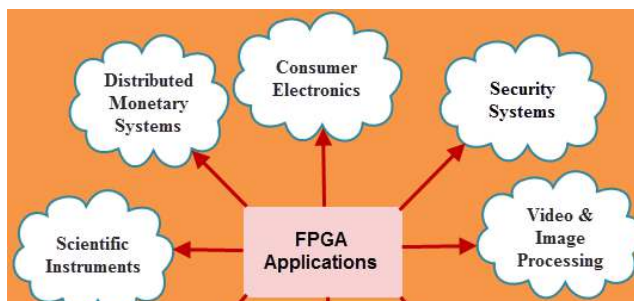
Program Features

- The program is split into lectures and labs sessions.
- Hands-on experience on basic & advanced-level topics.
- Interaction & learning with experts from academia & industry.
- Certificates to the participants by E&ICT Academy IITR.

Coordinators

Dr. Sanjeev Manhas
(PI, E&ICT Academy,
ECE Dept, IITR)

Dr. B.P. Das
(Coordinator,
ECE Dept, IITR)



Faculty Development Program
on
FPGA Based SoC Design
Sep 20, 2017 – Sep 21, 2017



REGISTRATION FORM

Applicant Name _____

Gender: _____

Category (GEN/OBC/SC/ST): _____

Designation: _____

Name and Address of the
Organizatio/Institute: _____

City/Town: _____

Email: _____

Phone Number: _____

Do you need Accommodation?
(Yes/No): _____

DD Number: _____

Date: _____

Issuing Bank: _____

Payable at: _____

Signature of the Applicant

Contact Us

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