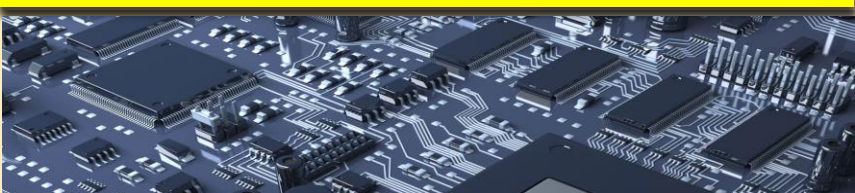# FPGA BASED SOC DESIGN (Oct. 16-24, 2016)

Organized by Electronics and ICT Academy IIT Roorkee

Experts from Academia/Industry

- ❖ **Dr. Gaurav Trivedi (IITG)** ❖ **Dr. B.P. Das (IITR)**
- ❖ **CoreELTechnologies**   ❖ **Dr. Sanjeev Manhas (IITR)**

Applications are invited from colleges/universities/institutes faculty members, research scholars, PG, with ECE/EE/CS/IT degree to attend a Training programme on **FPGA Based SOC Design Oct. 16-24, 2016.**
Venue: Uttarakhand Board of Technical Education, Training and Research Development cell, Uppar Aam Wala, Dehradun

Supported By

Department of Electronics and Information Technology (DeitY) Ministry of Communication and Information Technology, Government of India

About E&ICT Academy

- ❖ Electronics and Information Communication Technology (E&ICT) Academy is an initiative of Department of Electronics & Information Technology (DeitY) Ministry of Communications and IT, Govt. of India for the faculty/research scholar development program.
- ❖ E&ICT IITR is one of the seven such academies approved by Govt. of India from various IITs and NITs.
- ❖ E&ICT IITR aim to bring in depth erudition in form of theoretical and practical exposure to the trainees.
- ❖ The training would additionally cover development of entrepreneurship adeptness, which will facilitate cultivation start-ups in local region.

Benefits and Outcomes of the Course

- ❖ Learn the fundamentals of HDL programming.
- ❖ FPGA architecture along with FPGA design on latest FPGA boards.
- ❖ **XILINX VIVADO** system edition design flow.
- ❖ Hands-on experience conducted by experts from Industry and Academia. SOC based FPGA design project.
- ❖ Enhance employability by training individuals to find opportunities in FPGA based design applications in control, networking, security, image processing, application specific integrated circuits (ASIC), electronics design automation (EDA).

Course Program

Training Program is split in lectures and labs/hands-on sessions daily. **Course contents:**

- ❖ Digital Design Concepts and HDL Programming
- ❖ Timing Analysis, Hardware Debugging in FPGAs
- ❖ FPGA Architecture and Design Implementation using Xilinx Vivado
- ❖ Programmable SOC design and Hand on Sessions
- ❖ **Course evaluation by quizzes and project work**
- ❖ **Certificates with grades to participants by E&ICT Academy IIT Roorkee.**

Who Can Attend

Program is open to faculty members from colleges/universities, research scholars PG students and industry personnel working in the concerned/allied discipline.

How To Apply

Online – The participants may log on to the E&ICT academy IIT Roorkee website: http://eict.iitr.ac.in and fill up the application form.

By Email – Scanned copy of the filled application form duly endorsed by the forwarding authority is to be mailed at E&ICT academy email ids (eictiitr@gmail.com, eict@iitr.ac.in). Application format given in this brochure may also be downloaded from the website.

Registration Form

Name of the Applicant (first, last):
……………………………………

Gender: ……………………. …
Designation: ………………….
Name and Address of the Organization/Institute:
……………………………………………………
City/town:………………………………………
Email:…………………………………………..
Phone Number:…………………………………
Mobile Number:……………………………….
Do you need accommodation?
(Yes/No):……………………………...
DD Number:………………….Date:……………
Issuing Bank:………………payable at:…………
Signature of the applicant:…………………………

Signature and Seal of the Forwarding Authority (institute)
Name ....
Designation...

Affix passport size photograph

Registration Fee

Registration Fee (Including course materiel) :
Rs. 2000/- for participants from academia.
Rs. 3000/-from Industry and Research Organization.
Mode of Payment: Demand Draft in the name of "DEAN SRIC IIT ROORKEE"

Contact Details

For more details please log on to E&ICT academy IITR website: http://eict.iitr.ac.in
Course Coordinator: Dr. Sanjeev Manhas, IITR
Email: eictiitr@gmail.com , prateek2789@gmail.com
Phone No: +91-1332-28 6457, Mob. +91-7078627392