Dayananda Sagar University

Dayananda Sagar Institutions founded in the 60s by one such visionary, late Sri Dayananda Sagar committed to take knowledge to the people, transforms today's students into responsible citizens and professional leaders of tomorrow. Dayananda Sagar University (DSU) created by an Act of the Karnataka State in 2014, built on this adorable legacy and inspired by its own milestones, meeting the needs of quality higher education in this part of the world. Universities of great legacy across the world are the invaluable contribution of certain visionaries to the world. Universities don't manufacture products with specific use and determined life cycle.

School of Engineering under Dayananda Sagar University provides Science & Technology based Education that is required to develop high caliber Engineers suitable for Industry and Scientific Organization.

DSU Highlights

- Spread across a sprawling 130-acre campus with lush greenery and modern infrastructure.
- Located conveniently near Bengaluru city, providing easy access to the tech and cultural hub.
- Research, Innovation and Incubation (spread over 25,000 sft of industry quality labs) form the core of DSU.
- Advanced research centers focused on emerging technologies and innovation.
- Experienced faculty committed to combining theoretical and hands-on learning approaches.

Chief Patron

Dr. D Hemachandra Sagar
Chancellor, DSU

Dr. D Premchandra Sagar
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Patron

Dr. Amit Bhatt
Vice Chancellor, DSU

Dr. Puttamadappa C,
Registrar, DSU

Dr. Udaya Kumar Reddy KR,
Dean-SOE, DSU

Principal Investigator

Prof. Sanjeev Manhas
ECE Department, IIT Roorkee

FDP Organizers

Prof. Sanjeev Manhas
ECE Department, IIT Roorkee

Dr. Arun Balodi
Professor and Chairman
Department of ECE, DSU Bengaluru India

FDP Coordinators

Dr. Sneha Sharma
Assistant Professor,
Department of ECE, DSU Bengaluru India

Dr. Shirshendu Roy
Assistant Professor,
Department of ECE, DSU Bengaluru India

Dr. Pushpamala S
Associate Professor and IEEE Branch Counsellor
Department of ECE, DSU Bengaluru India

How to make Payment

Please make the payment first using the below link upload the payment receipt when filling out the Google registration form

Payment Link

Conference Code- EICTIITR-FDP-25-34

Fees: ₹ 250/- Faculty/Research Scholar/ Students
₹ 500/- Industry/Others
Note: Refund will be done in case of course cancellation only, with in 20 working days

Registration Details

Click Here

An Initiative
of
Ministry of Electronics & Information Technology (MeitY),
Government of India

FIVE -DAYS
FACULTY DEVELOPMENT PROGRAM
ON

EMERGING TRENDS IN FPGA-ACCELERATED MACHINE LEARNING IN VLSI

10 Feb 2025 - 14 Feb 2025

ORGANIZED BY

ELECTRONICS AND COMMUNICATION ENGINEERING DEPARTMENT
SCHOOL OF ENGINEERING

Venue: Hybrid Mode

in association with

CEDA®
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BANGALORE CHAPTER

Dayananda Sagar University

Devarakaggalahalli, Harohalli Kanakapura Road, Dt, Ramanagara, Karnataka 562112
https://www.dsu.edu.in/

ABOUT ECE

The Department of Electronics and Communication Engineering, established in 2015, offers a B.Tech program with a capacity of 120 students and an M.Tech program in Embedded Systems for 18 students. Our curriculum effectively combines fundamental principles with practical applications through rigorous lab work and challenging projects. This approach cultivates strong problem-solving skills and prepares graduates for successful careers in diverse fields such as Signal Processing, Communication Systems, VLSI Design, Embedded Systems, and Robotics. A highly qualified faculty, the majority holding Ph.D.s from renowned institutions both in India and abroad, brings extensive research experience to the department. This expertise makes them ideal mentors for Ph.D. students, and the department is dedicated to providing a stimulating research environment that encourages high-quality research.

EICT Academy IIT Roorkee

India is fast emerging as a world power in Information, Communication Technology, and Electronics (ICTE) sector. To complement its growth and further development, there is an ever-increasing need for trained professionals with specialization in this space. This includes training of professionals not only in existing and advanced technologies but also in the fields of electronic manufacturing. To meet this objective MeitY has approved a scheme and set up Electronics and ICT (E&ICT) Academy at IIT Roorkee and other IITs/ NITs.

ABOUT FDP

This FDP is a 5-day online programme aimed to enhance research capabilities primarily for the researchers and professionals working in FPGA, Machine Learning in VLSI domain. Eminent Scientists and Professors from renowned institutes will share their research experience with and experts from industries will share budding researchers offer Hands-on-Sessions.

OBJECTIVES OF FDP

- To promote R & D activities in the areas of advanced FPGA, Machine Learning in VLSI.
- To motivate, educate and train participants to innovate in the field of FPGA, Machine Learning in VLSI through practical demonstration.
- To provide a forum to exchange views, ideas in carrying out research, testing and consultancy in the area of FPGA, Machine Learning in VLSI.

TOPICS TO BE COVERED

Key topics include:
- Fundamentals of FPGAs and High-Level Synthesis (HLS)
- Machine Learning Algorithms for FPGAs.
- Hardware/Software Co-design for ML Acceleration
- Emerging Trends: Investigate cutting-edge topics like in-memory computing, approximate computing, and AI accelerators for edge devices.
- Hands-on Labs and Projects: Gain practical experience through hands-on labs and mini-projects using industry-standard FPGA development boards and tools.

WHO SHOULD ATTEND?

Faculty members and research scholars of different Universities/ Colleges from Engineering and Sciences This program will be especially helpful to those participants who are doing or planning to start a research career in FPGA, Machine Learning in VLSI.

RESOURCE PERSONS

Dr. Sanjeev Manhas
Professor, Indian Institute of Technology, Roorkee, India.

Dr. C.P. Ravikumar
Adjunct Professor, Indian Institute of Technoloy Dharwad, India.

Dr. Hariprasad Bhat
Manager – RTL Design & FPGA Implementation at Lekha, Bengaluru, India.

Dr. Sivasankaran K
Professor, Vellore Institute of Technology, India.

Dr. Samba Raju Chiluveru
Indian Institute of Technology, Dharwad, India.

Dr. Arunachalam V
Associate Professor, Vellore Institute of Technology, India.

Dr. Sivaramakrishnan
Associate Professor, Presidency University, India.

Dr. Tushar Tyagi
Senior FPGA Engineer, Firmware Engineer, Sperry Marine, United Kingdom.

Dr. Anand Mukhopadhyay
Mathwork Pvt Ltd, Bengaluru, India.

Dr. Shirshendu Roy
Assistant Professor, Dayananda Sagar University, Bengaluru, India.

Dr. Gayathri K M
Associate Professor, Dayananda Sagar University, Bengaluru, India.

Mr. Abhinav Karan
Assistant Professor, Dayananda Sagar University, Bengaluru, India.

Certification

E-Certificate will be provided to all registered participants based on the attendance.

Important Dates:

Last day for registration: 08/02/2025
FDP Schedule: 10th Feb - 14th Feb, 2025